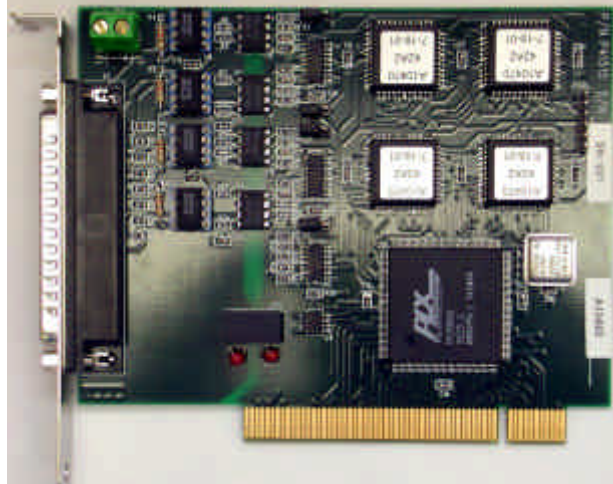




Product Specification for Model PCI-4



The Model PCI-4 is a PCI bus form factor and bus compatible module that provides four channels of magnetostrictive transducer interface. When commanded by the PC, the module interrogates the transducers and converts the timing information to position values. The module can be configured to operate with Start/Stop, Trailing Edge Start/Stop, Pulse Width Modulated or SSI transducers. Each Start/Stop channel can support one to thirty one recirculations or from one to thirty one magnets. All operations are controlled by the host PC software. A 56 MHz oscillator provides excellent resolution; 0.002 inches using one recirculation or in the multi-magnet mode. The transducers are electrically isolated from the PC power supply. Transducer connections are made via a 37 pin DSUB connector on the rear of the board. Transducer power can be brought to the board on a two-position screw terminal connector where it is distributed to the J1 connector.

FEATURES

- PCI Plug and Play operation with no dipswitches to set.
- Supports from 1 to 4 magnetostrictive transducers
- Can be configured with any combination of Start/Stop, Pulse Width Modulated or SSI transducers
- 56 MHz Count oscillator provides 0.001 inch resolution with 2 recirculations
- 24 bit position counter for each transducer
- 24 or 25 bit SSI position
- Number of recirculations or magnets software selectable from 1 to 15
- Multiple magnet mode measures from 1 to 31 magnets on each of the transducers
- Optical isolation between the transducer and the board to prevent damage to PC
- Cables directly to the transducer
- The 24V transducer power can be distributed by the board
- Separate 2 pin screw terminal for +24V transducer power
- Individual status registers provides ready indication for each channel
- Half length PCI board is 5.2 inches long

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***SPECIFICATIONS***

- PCI 2.1 compatible
- 56 MHz oscillator provides 0.001 inch resolution with 2 recirculations
- Multi-magnet mode requires Start Stop transducer and must be interrogated once for each magnet
- Magnet number in Multi-magnet mode is software settable from 1 to 31
- 37 pin DSUB connector to the transducers
- Transducer I/O is RS422 compatible
- The Gate transducer inputs to the board are terminated in 120 ohms
- Re-circulations are software settable from 1 to 31.
- Requires an externally interrogated transducer for PWM operation
- Occupies 16 bytes of I/O space
- Power Requirements: 5 VDC @ < 500 ma.
- Size: 5.2 x 4.2 inch (132 x 107 mm).

Model numbering

Specify Model: **PCI-4-ABCD**

- A** - Channel A type
- B** - Channel B type
- C** - Channel C type
- D** - Channel D type

Substitute one of the following for the channel type:

- R** - Leading Edge Start/Stop or PWM*
- S** - SSI
- T** - Trailing Edge Start/Stop or PWM*
- N** - None

For example, a board with two start/stop interfaces on channels A and B and two SSI transducers on channels C and D would be model **PCI-4-RRSS**.

*PWM operation requires an externally interrogated PWM transducer.

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1 Installation

1.1 Overview

The installation consists of two portions, the driver install and the SDK install. The driver install installs a driver that allows Windows applications to access the PCI4 hardware. The SDK install installs the DLLs and header files that provide an API allowing programs to use the PCI4 easily. See the PCI4 SDK Programmer's Reference for more information. The Windows NT installation combines these steps.

Note that the PCI4 is a Plug and Play device and has no jumpers to be set. The Plug and Play Bios will automatically assign the PCI4 an address space.

1.2 Installing the board

Install the PCI4 in an unused PCI slot. If you are using Windows 98, proceed to section 1.2.1. If you are using Windows NT, proceed to section 1.2.2. If you are using Windows 2000, proceed to section 1.2.3. Currently the PCI4 is not supported in Windows 95 or Windows ME. No special installation is required for DOS applications.

1.2.1 Windows 98 Driver Installation

After booting for the first time with the PCI4 installed, Windows will detect the PCI4 as an unknown PCI device. If this does not happen, use the Add New Hardware applet from the control panel.

1. After detection, the search for drivers dialog will open. Select "Next".
2. Select "Search for a suitable driver" and press "Next".
3. Uncheck all choices except "Specify a location". Enter the location of the PCI-4 SDK CD (e.g. D:\) and press "Next".
4. Windows should find the correct driver at this point. Press "Next". If Windows cannot locate the driver, check that the location entered in step 4 is correct.
5. Press finish after Windows installs the driver.

Continue with section 1.2.4.

1.2.2 Windows NT 4.0 Driver Installation

Skip to section 1.2.4. Setup.exe will install the drivers necessary for Windows NT.

1.2.3 Windows 2000 Driver Installation

After booting for the first time with the PCI4 installed, Windows will detect the PCI4 as an unknown PCI device. If this does not happen, use the Add/Remove Hardware applet from the control panel.

1. After detection, the search for drivers wizard will open. Select "Next".
2. Select "Search for a suitable driver" and press "Next".
3. Uncheck all choices except "Specify a location". Select "Next".
4. Enter the location of the PCI-4 SDK CD (e.g. D:\) and select "OK".

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5. Windows should find the correct driver at this point. Press “Next”. If Windows cannot locate the driver, check the location entered in step 4 is correct.
6. Press finish after Windows installs the driver.

Continue with section 1.2.4.

1.2.4 SDK Installation

Run setup.exe from the installation CD. This will install the PCI4 SDK and DLLs. The SDK files are installed to C:\pci4 (or x:\pci4 where x is the boot drive letter, if the boot drive is not C.) Reboot the computer before attempting to use the PCI4 SDK.

1.2.5 Dos Installation

There is no special procedure for DOS installation. The files in the SDKFiles directory of the PCI4 CD can be copied to the hard drive for use.

2 Theory of operation

2.1 Board functions and addressing

The board is comprised of five logical sections, the PCI section and a Rapid Controls EPLD for each channel. The PCI9052 interface chips reads its’ setup information from the 93LC46B EEPROM which is factory programmed.

2.1.1 Address decoding

The setup from the EEPROM defines the local bus space as 16 bytes of I/O. Address decoding is provided by the PCL9052 PCI interface chip. When the board is installed the operating system or Bios assigns an address to the board. All access to the four transducer interfaces must be through the address space provided by the PCI Bios.

2.1.2 I/O map

The PCI-4 board occupies 16 bytes of I/O space, the base address of which is assigned by the motherboard’s PCI controller. The I/O map is divided into four 4 byte sections. Channel 1 occupies the first four bytes, channel 2 the next four etc.

| Address | Function | 2^0 | 2^1 | 2^2 | 2^3 | 2^4 | 2^5 | 2^6 | 2^7 |
|----------|--------------------------------|---------------|---------------|---------------|---------------|---------------|---------------|---------------|---------------|
| Base + 0 | Rd transducer 1 data low byte | Data 2^0 | Data 2^1 | Data 2^2 | Data 2^3 | Data 2^4 | Data 2^5 | Data 2^6 | Data 2^7 |
| Base + 1 | Rd transducer 1 data mid byte | Data 2^8 | Data 2^9 | Data 2^{10} | Data 2^{11} | Data 2^{12} | Data 2^{13} | Data 2^{14} | Data 2^{15} |
| Base + 2 | Rd transducer 1 data high byte | Data 2^{16} | Data 2^{17} | Data 2^{18} | Data 2^{19} | Data 2^{20} | Data 2^{21} | Data 2^{22} | Data 2^{23} |
| Base + 3 | Rd transducer 1 status | See section | | | | | | | |
| Base + 3 | Wr transducer 1 command | See section | | | | | | | |
| Base + 4 | Rd transducer 2 data low byte | Data 2^0 | Data 2^1 | Data 2^2 | Data 2^3 | Data 2^4 | Data 2^5 | Data 2^6 | Data 2^7 |
| Base + 5 | Rd transducer 2 | Data | Data | Data | Data | Data | Data | Data | Data |

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| | | | | | | | | | |
|-----------|-----------------------------------|------------------------|------------------------|-------------------------|-------------------------|-------------------------|-------------------------|-------------------------|-------------------------|
| Base + 6 | data mid byte Rd transducer 2 | 2 ⁸ Data | 2 ⁹ Data | 2 ¹⁰ Data | 2 ¹¹ Data | 2 ¹² Data | 2 ¹³ Data | 2 ¹⁴ Data | 2 ¹⁵ Data |
| Base + 7 | data high byte Rd transducer 2 | 2 ¹⁶ | 2 ¹⁷ | 2 ¹⁸ | 2 ¹⁹ | 2 ²⁰ | 2 ²¹ | 2 ²² | 2 ²³ |
| Base + 7 | status Wr transducer 2 | See section | | | | | | | |
| Base + 7 | command Wr transducer 2 | See section | | | | | | | |
| Base + 8 | Rd transducer 3 | Data | Data | Data | Data | Data | Data | Data | Data |
| Base + 8 | data low byte | 2 ⁰ | 2 ¹ | 2 ² | 2 ³ | 2 ⁴ | 2 ⁵ | 2 ⁶ | 2 ⁷ |
| Base + 9 | Rd transducer 3 | Data | Data | Data | Data | Data | Data | Data | Data |
| Base + 9 | data mid byte | 2 ⁸ | 2 ⁹ | 2 ¹⁰ | 2 ¹¹ | 2 ¹² | 2 ¹³ | 2 ¹⁴ | 2 ¹⁵ |
| Base + 10 | Rd transducer 3 | Data | Data | Data | Data | Data | Data | Data | Data |
| Base + 10 | data high byte | 2 ¹⁶ | 2 ¹⁷ | 2 ¹⁸ | 2 ¹⁹ | 2 ²⁰ | 2 ²¹ | 2 ²² | 2 ²³ |
| Base + 11 | Rd transducer 3 | See section | | | | | | | |
| Base + 11 | status Wr transducer 3 | See section | | | | | | | |
| Base + 11 | command Wr transducer 3 | See section | | | | | | | |
| Base + 12 | Rd transducer 4 | Data | Data | Data | Data | Data | Data | Data | Data |
| Base + 12 | data low byte | 2 ⁰ | 2 ¹ | 2 ² | 2 ³ | 2 ⁴ | 2 ⁵ | 2 ⁶ | 2 ⁷ |
| Base + 13 | Rd transducer 4 | Data | Data | Data | Data | Data | Data | Data | Data |
| Base + 13 | data mid byte | 2 ⁸ | 2 ⁹ | 2 ¹⁰ | 2 ¹¹ | 2 ¹² | 2 ¹³ | 2 ¹⁴ | 2 ¹⁵ |
| Base + 14 | Rd transducer 4 | Data | Data | Data | Data | Data | Data | Data | Data |
| Base + 14 | data high byte | 2 ¹⁶ | 2 ¹⁷ | 2 ¹⁸ | 2 ¹⁹ | 2 ²⁰ | 2 ²¹ | 2 ²² | 2 ²³ |
| Base + 15 | Rd transducer 4 | See section | | | | | | | |
| Base + 15 | status Wr transducer 4 | See section | | | | | | | |
| Base + 15 | command Wr transducer 4 | See section | | | | | | | |

2.2 Start/Stop, PWM transducers

The host PC104 processor writes a recirculation value and mode to the Control/Status register, loading the recirculation count. The control register write causes an interrogation pulse to be issued from the EPLD which in turn causes a 1 microsecond pulse to be issued from the Interrogate + and - outputs of the board. The transducer immediately responds with a 1 microsecond Gate + and - pulse. This pulse increments the recirculation register and starts the position counter counting. After the time required for the torque pulse to propagate down the transducer from the magnet (9 * inches microseconds) another Gate + and Gate - pulse is returned from the transducer. This again increments the recirculation counter. If it is not the last recirculation another one microsecond interrogate pulse is issued. This process continues until the last interrogation when the last Gate+ and Gate - pulse returned stops the position counter and sets the process completed flag. The 24 bit counter can now be read and will hold a value of (inches * Gradient * 56 * recirculations).

When operating in the PWM mode the counter counts from the leading edge to the trailing edge of the Gate pulse. The multi-magnet mode functions the same as the recirculation mode except that only one interrogate pulse is issued. The magnet measured is determined by the recirculation count programmed. Since only a single magnet can be measured at one time, the transducer must be interrogated once for each new magnet position desired. The software must wait each time for all of the magnets to respond, this is best accomplished by waiting a time equal to 9 * (length of the transducer + 10) microseconds between interrogations.

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2.3 Channel Registers (base +0 , +1, + 2) for all channels

| | 2 ⁰ | 2 ¹ | 2 ² | 2 ³ | 2 ⁴ | 2 ⁵ | 2 ⁶ | 2 ⁷ |
|------------------|----------------------|----------------------|----------------------|----------------------|----------------------|----------------------|----------------------|----------------------|
| Read Base + 0 | Data 2 ⁰ | Data 2 ¹ | Data 2 ² | Data 2 ³ | Data 2 ⁴ | Data 2 ⁵ | Data 2 ⁶ | Data 2 ⁷ |
| Read Base + 1 | Data 2 ⁸ | Data 2 ⁹ | Data 2 ¹⁰ | Data 2 ¹¹ | Data 2 ¹² | Data 2 ¹³ | Data 2 ¹⁴ | Data 2 ¹⁵ |
| Read Base + 2 | Data 2 ¹⁶ | Data 2 ¹⁷ | Data 2 ¹⁸ | Data 2 ¹⁹ | Data 2 ²⁰ | Data 2 ²¹ | Data 2 ²² | Data 2 ²³ |

2.3.1 Programming (Start/Stop and PWM), Command Data Register (base + 3)

| | 2 ⁰ | 2 ¹ | 2 ² | 2 ³ | 2 ⁴ | 2 ⁵ | 2 ⁶ | 2 ⁷ |
|------------------------------|------------------------|------------------------|------------------------|------------------------|------------------------|----------------|-------------------------|---------------------|
| Write Command Base + 3 | Recircs 2 ⁰ | Recircs 2 ¹ | Recircs 2 ² | Recircs 2 ³ | Recircs 2 ⁴ | PWM Mode | Multi magnet Mode | Interrupt Enable |
| Read Status Base + 3 | Recircs 2 ⁰ | Recircs 2 ¹ | Recircs 2 ² | Recircs 2 ³ | Recircs 2 ⁴ | PWM Mode | Multi magnet Mode | Data Ready |

The PWM mode bit should be set to 1 for PWM transducers and 0 for Start Stop.

The Multi-magnet bit should be set to 1 for multi magnet operation and 0 for single magnet operation.

The PWM and Multi-magnet bits are mutually exclusive. Setting them both to 1 will lead to undefined results.

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2.4 SSI transducers

The host PC processor writes a bit length value to the Control/Status register causing the clock generator to start the clock data process. The board generates the proper number of clock pulses at 218 KHz and clocks in the data response from the transducer. When all 24 or 25 bits have been clocked into the registers the data ready flag is set. If interrupts were enabled the interrupt line is driven high. Data is available in the four registers. The transducer controls the resolution and data format, Gray code or binary.

2.4.1 Programming (SSI), Data Registers

| | 2 ⁰ | 2 ¹ | 2 ² | 2 ³ | 2 ⁴ | 2 ⁵ | 2 ⁶ | 2 ⁷ |
|------------------|----------------------|----------------------|----------------------|----------------------|----------------------|----------------------|----------------------|----------------------|
| Read Base + 0 | Data 2 ⁰ | Data 2 ¹ | Data 2 ² | Data 2 ³ | Data 2 ⁴ | Data 2 ⁵ | Data 2 ⁶ | Data 2 ⁷ |
| Read Base + 1 | Data 2 ⁸ | Data 2 ⁹ | Data 2 ¹⁰ | Data 2 ¹¹ | Data 2 ¹² | Data 2 ¹³ | Data 2 ¹⁴ | Data 2 ¹⁵ |
| Read Base + 2 | Data 2 ¹⁶ | Data 2 ¹⁷ | Data 2 ¹⁸ | Data 2 ¹⁹ | Data 2 ²⁰ | Data 2 ²¹ | Data 2 ²² | Data 2 ²³ |

2.4.2 Programming (SSI), Command / Status Registers (Base + 3)

| | 2 ⁰ | 2 ¹ | 2 ² | 2 ³ | 2 ⁴ | 2 ⁵ | 2 ⁶ | 2 ⁷ |
|------------------------------|----------------------|----------------|----------------|----------------|----------------|--|----------------|---------------------|
| Write Command Base + 3 | N/A | N/A | N/A | N/A | N/A | Set for 25 bit. Clear for 24 bit | N/A | Interrupt Enable |
| Read Status Base + 3 | Data 2 ²⁴ | 0 | 0 | 0 | 0 | 0 | N/A | Data Ready |

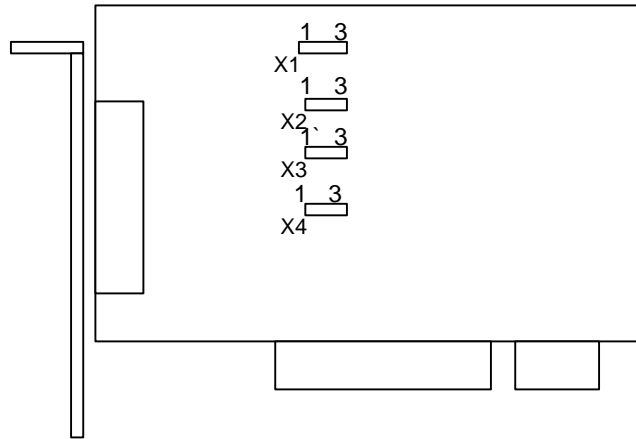
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3 Jumpers



3.1 Jumper map

3.2 Jumpers X1 through X4 (Factory Set)

Jumpers X1 through X4 are factory set and must be set according to the type of transducer connected to channel A through F respectively. Connect 1 to 2 for Start/Stop and PWM type transducer. Connect 2 to 3 for SSI transducers.

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4 Connections

4.1 J1, 32 bit PCI bus connector, required.

4.2 P1, 37 pin DSUB transducer connector.

| | | | |
|----|-------------------|----|------------------|
| 1 | Gate A - | 20 | Gate A + |
| 2 | Interrogate A+ | 21 | Interrogate A - |
| 3 | Transducer ground | 22 | Transducer Power |
| 4 | Gate B - | 23 | Gate B + |
| 5 | Interrogate B + | 24 | Interrogate B - |
| 6 | Transducer ground | 25 | Transducer Power |
| 7 | Gate C - | 26 | Gate C + |
| 8 | Interrogate C + | 27 | Interrogate C - |
| 9 | Transducer ground | 28 | Transducer Power |
| 10 | Gate D - | 29 | Gate D + |
| 11 | Interrogate D + | 30 | Interrogate D - |
| 12 | Transducer ground | 31 | Transducer Power |
| 13 | | 32 | |
| 14 | | 33 | |
| 15 | Transducer ground | 34 | Transducer Power |
| 16 | | 35 | |
| 17 | | 36 | - |
| 18 | Transducer ground | 37 | Transducer Power |
| 19 | Transducer ground | | |

Transducer power on pins 22, 25, 28, 31, 34 and 37 is not required by the PC4-TEMPO board but can be used to connect the + voltage from P2 to the transducer (Red).

4.3 P2, 2 position screw terminal

| | |
|---|--------------------------|
| 1 | + Transducer power input |
| 2 | - Transducer ground |

Transducer power can be supplied to the transducers via P2 pin 1. A diode prevents accidentally applying a negative voltage to the transducers. Transducer ground is connected via P2 pin 2.

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4.4 Optional cable termination

Rapid Controls can provide Model CAB-TERM37 which provides a 10 ft cable and a screw terminal board. This allows connecting wires to the PCI-4 via a screw terminal rather than creating a cable. Pin numbering on the screw terminals is the same as the P1 connector defined above.



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